

66/L2/80

Case Packet No. P8075-9014  
Date August 27, 1999

A

ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the patent application of  
Inventor(s): Naoharu SHINOZAKI

For: INPUT CIRCUIT HAVING CURRENT REGULATING TRANSISTOR

Jc519 U.S. PTO  
09/385014  
08/27/99

- ☒ Specification and Claims (27 pages)  
☒ 6 sheets of drawings  
☒ Newly executed Declaration and Power of Attorney  
☒ Return Receipt Postcard  
☒ An assignment of the invention to FUJITSU LIMITED with accompanying PTO-1595 Form  
☒ A certified copy of Japanese Patent Application No. 10-285286, filed: October 7, 1998  
☐ A verified stmt. to establish small entity status under 37 C.F.R. §1.9 and 37 C.F.R. §1.27  
☐  
☒ A filing fee, calculated as shown below:

	(Col. 1)	(Col. 2)
FOR:	No. Filed	No. Extra
BASIC FEE		
TOTAL CLAIMS	20 - 20 =	* 0
INDEP CLAIMS	3 - 03 =	* 0
MULTIPLE DEPENDENT CLAIM PRESENTED		

If the difference in Col. 1 is less than zero, enter "0" in  
Col. 2

Small Entity	
RATE	FEE
	\$380
x 9 =	
x 39 =	
+130 =	
TOTAL	

or

or

or

or

or

Other Than A Small Entity	
RATE	FEE
	\$760
x 18 =	0
x 78 =	0
+260 =	0
	\$760

Check # 16501 in the amount of \$ 800.00 to cover the filing fee and assignment recordation fee.  
In the event that the attached check is found to be insufficient, the Commissioner is hereby authorized to charge  
payment for any additional filing fees required under 37 CFR 1.16 associated with this communication or credit  
any over-payment to Deposit Account No. 14-1060.

Respectfully submitted,

NIKAIDO, MARMELSTEIN, MURRAY &amp; ORAM LLP

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GEO:mmg